

fig. 1A

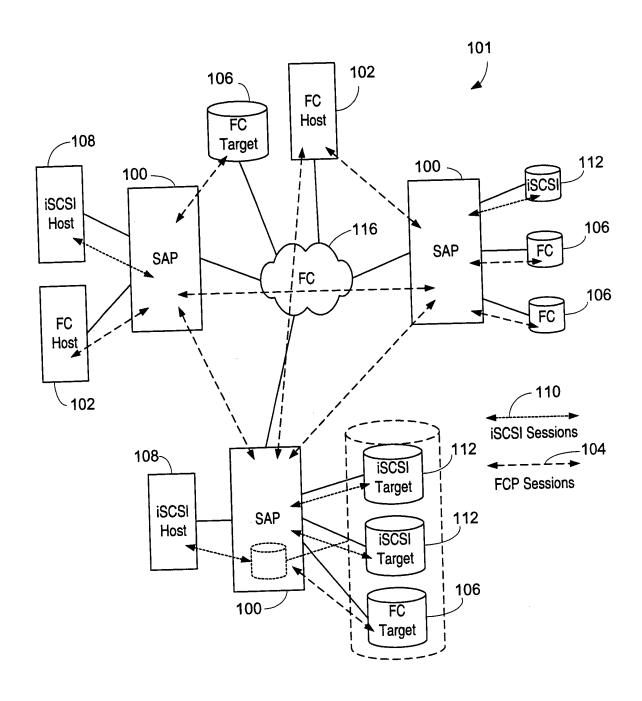


fig. 18

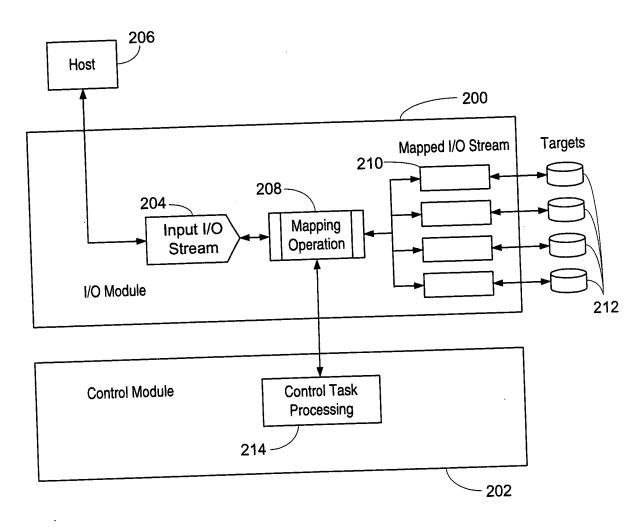


fig. 2

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Industry APIs Topology & Discovery Routines Network Management

Fibre Channel Connectivity IP Connectivity Processor Management Processor Processor

Firmware

I/O Processor

Custom Application Segments Virtualization Engine

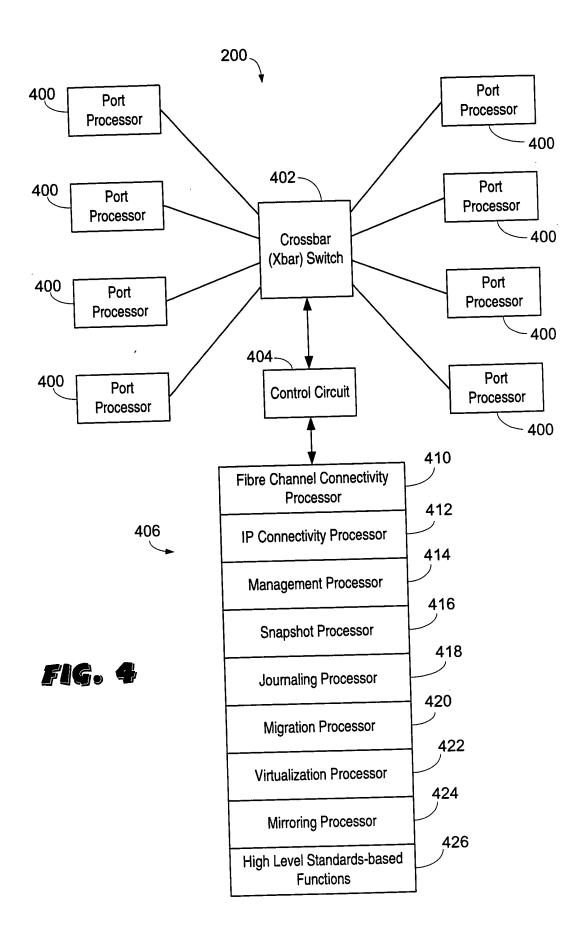
Port Processors

Custom Application Segments

Crossbar Switch

Silicon





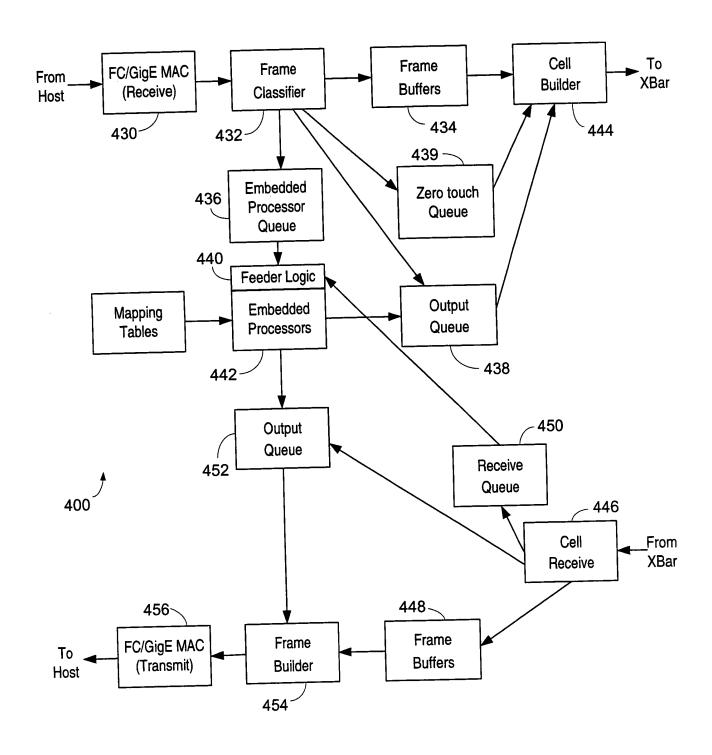


fig. 5

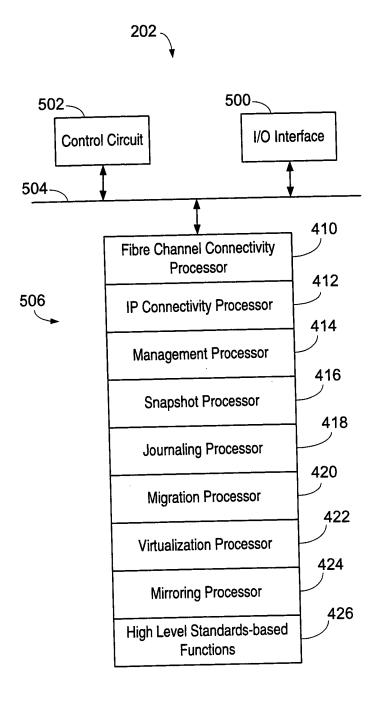


fig. 6

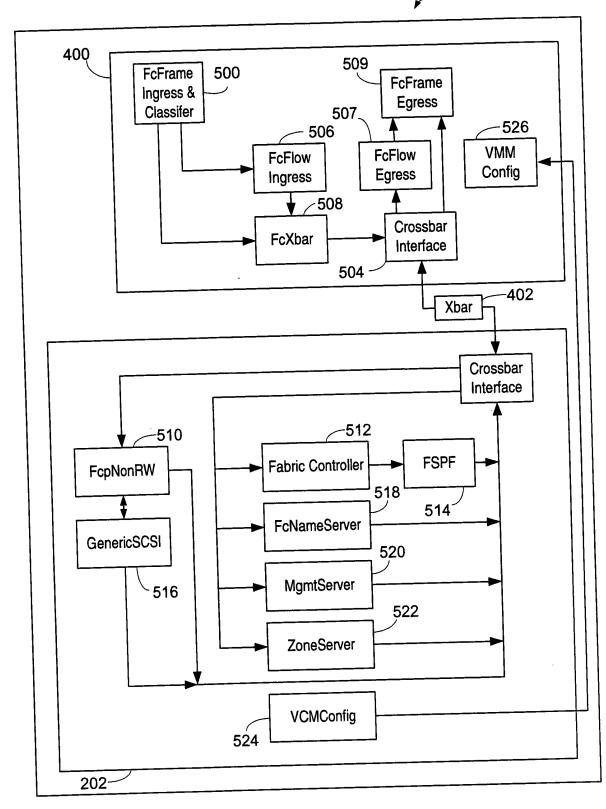


FIG. I

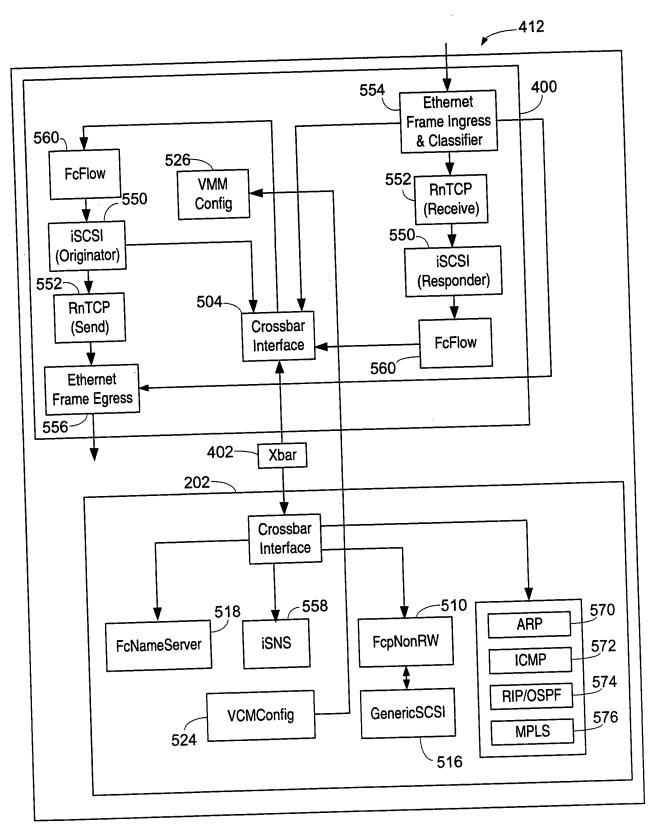
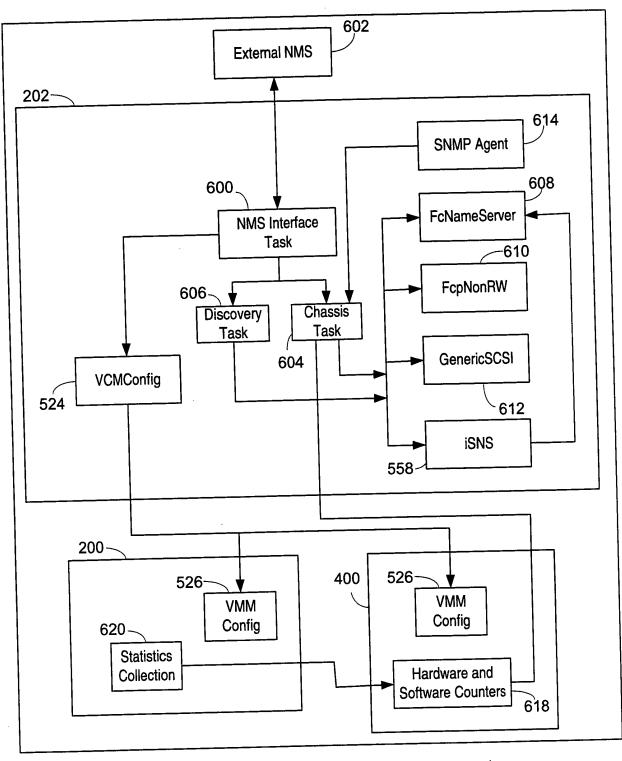


fig. 8



<u>~414</u>

fig. 9

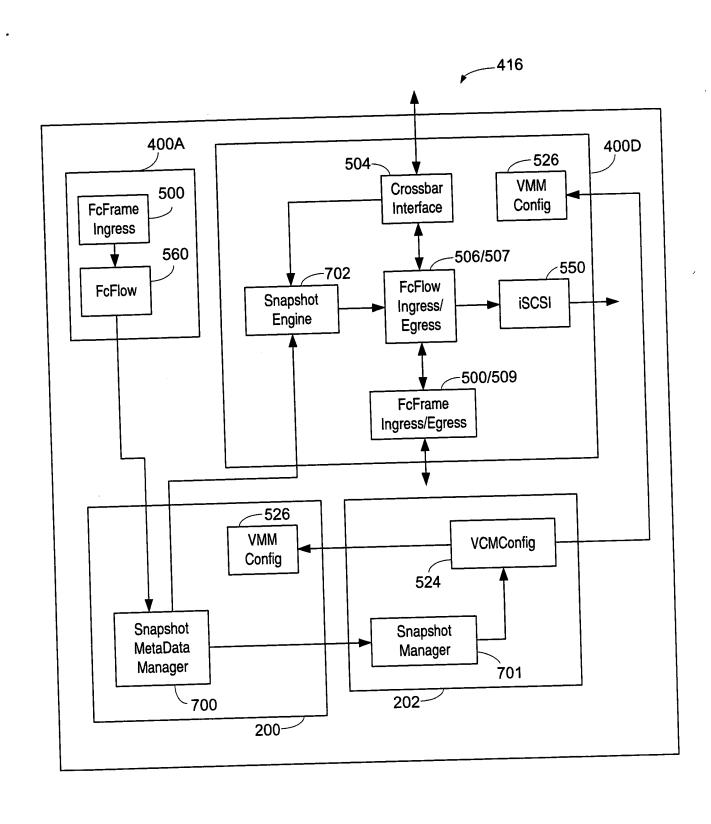


FIG. 10

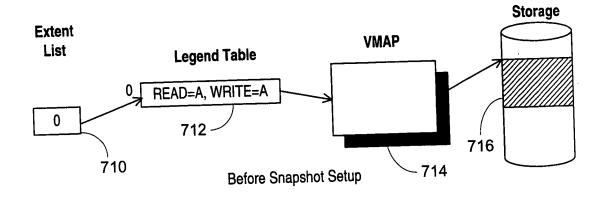


FIG. 11

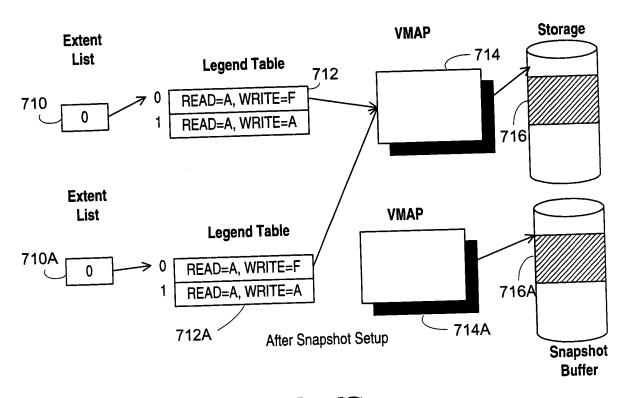


FIG. 12

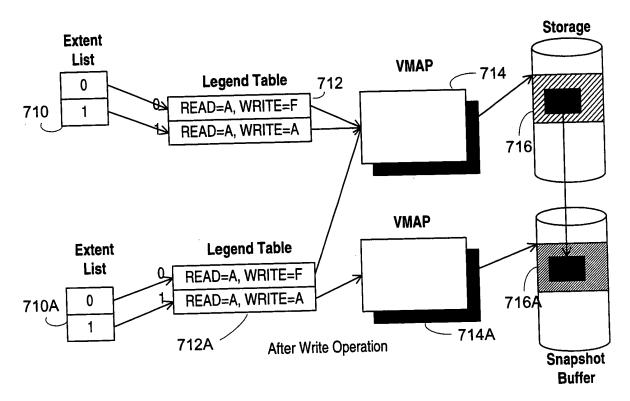
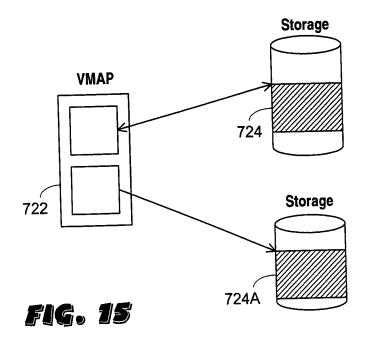
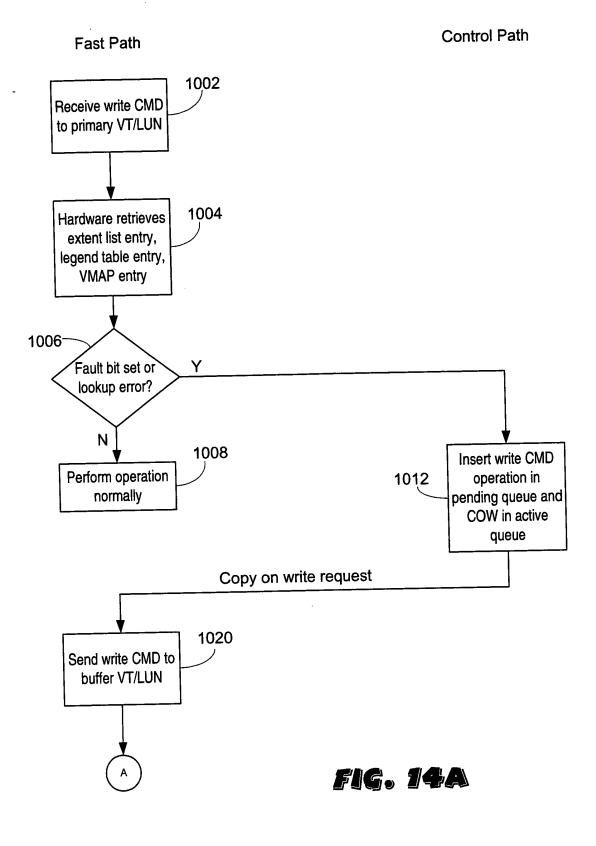
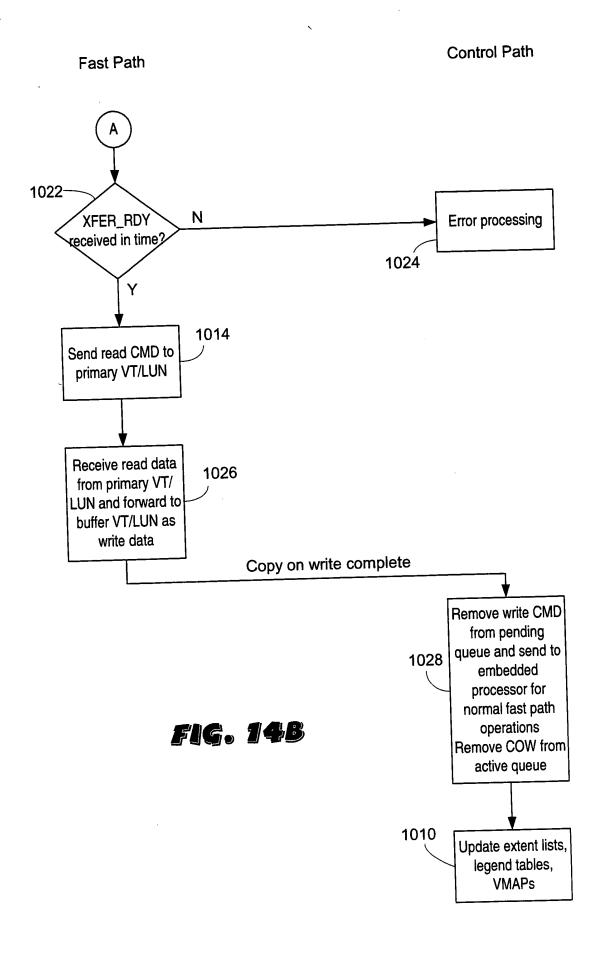
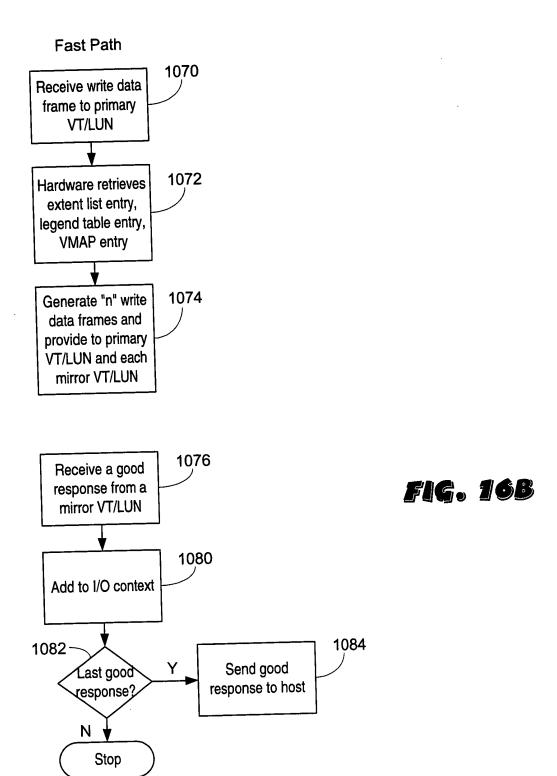


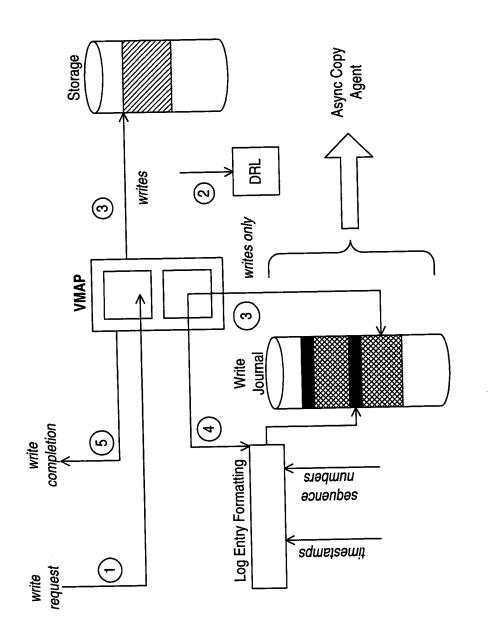
FIG. 13



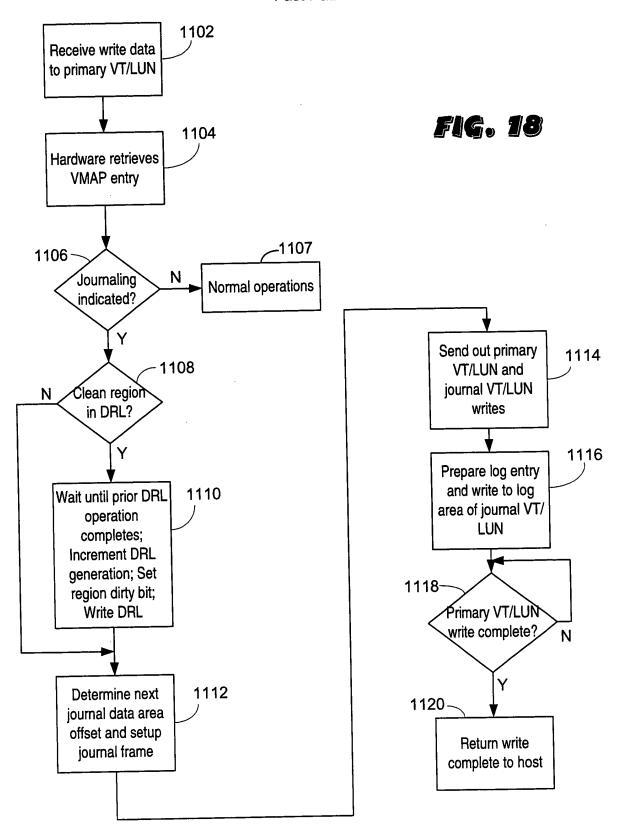








Fast Path



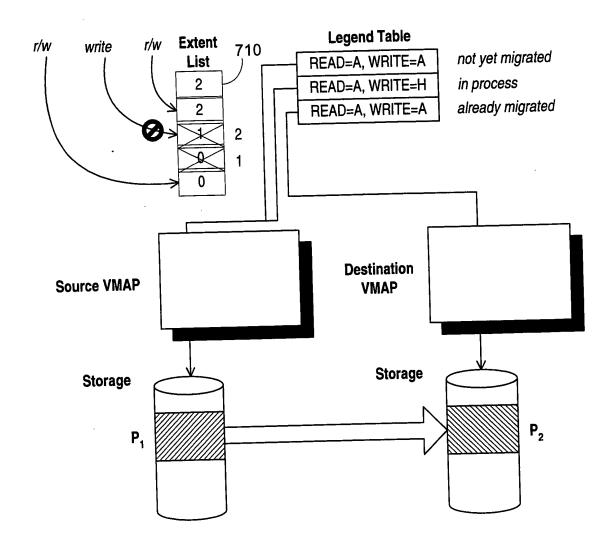


FIG. 19

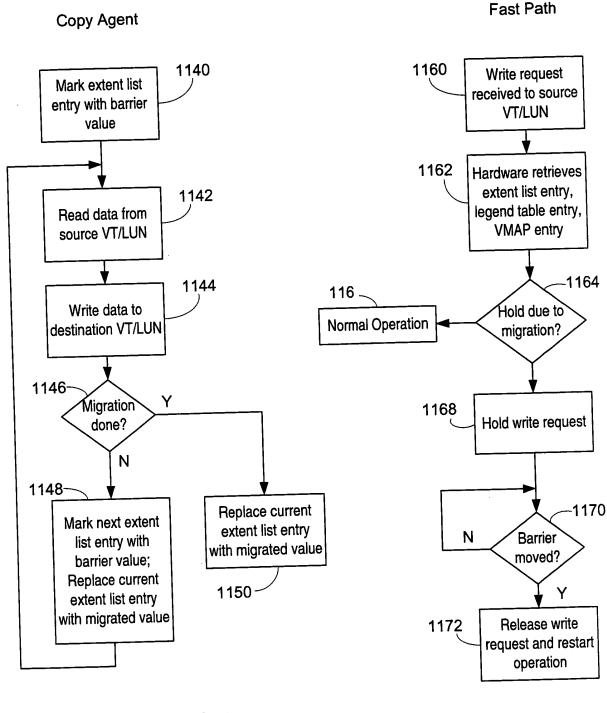
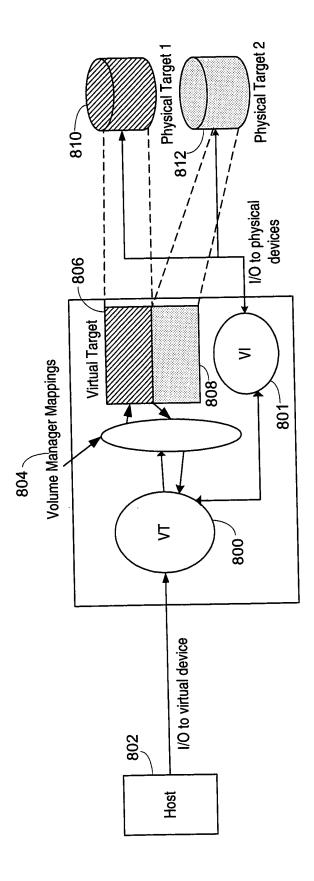
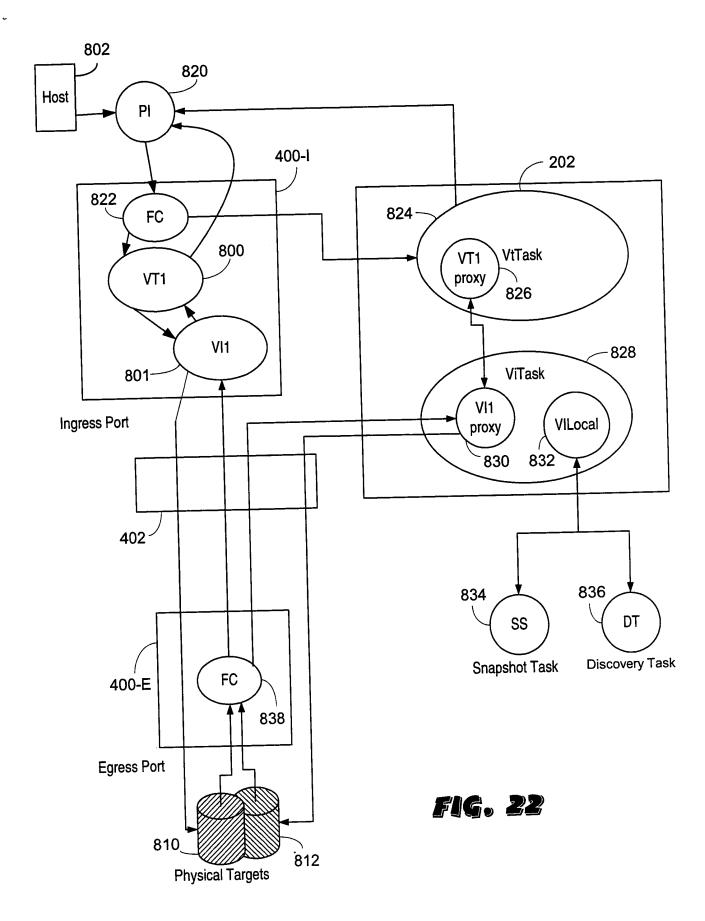


fig. 20A

FIG. 20B







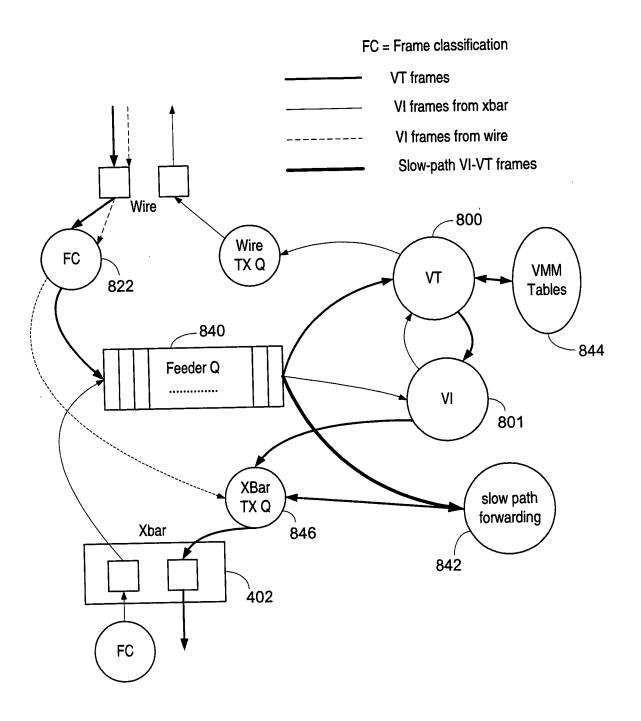
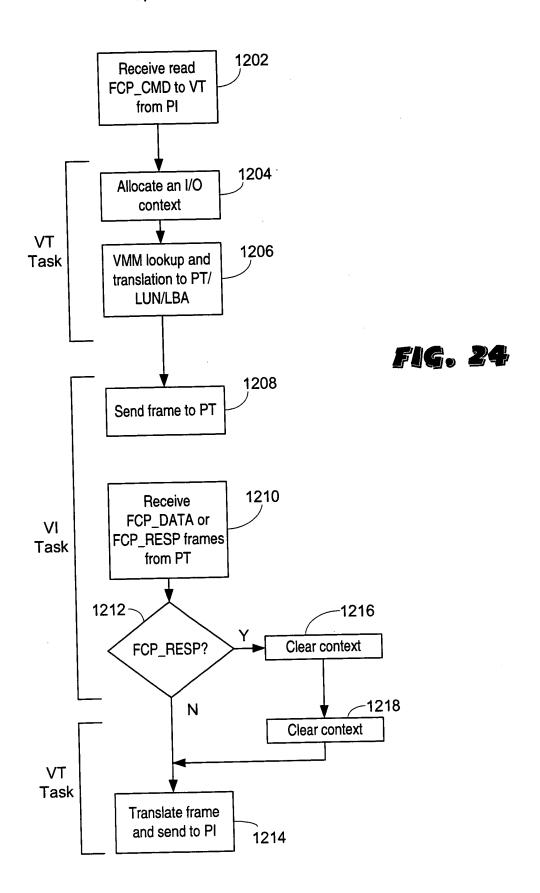


FIG. 23



Simple Write

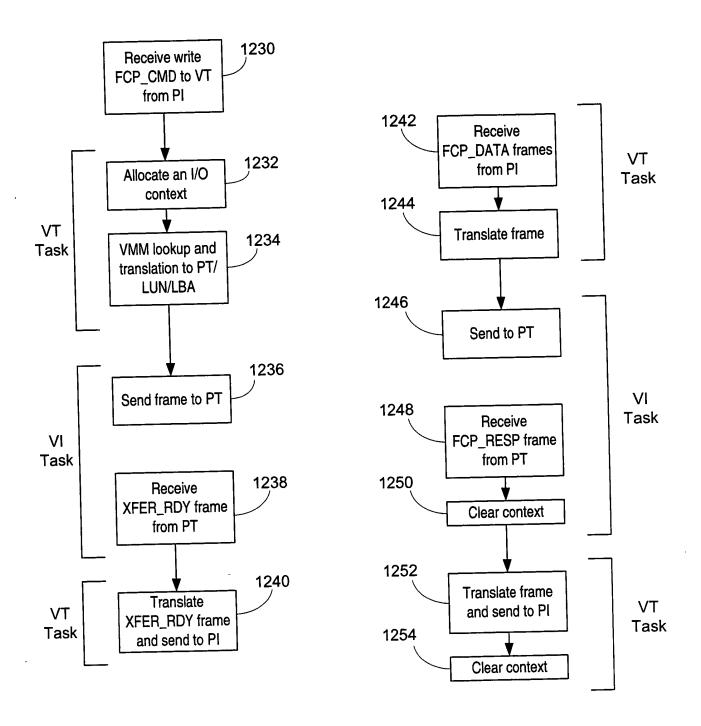


FIG. 25

Spanned Read

